IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	10/671,922
Filing Date	
Inventor	
Assignee	Micron Technology, Inc.
Group Art Unit	2812
Examiner	Ahmadi, Mohsen
Attorney's Docket No	
Title: Atomic Layer Deposition Methods, and Methods of Forming Materials Over	
Semiconductor Substrates	Ğ

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

The attached Form PTO/SB/08a is submitted in compliance with 37 C.F.R. §§ 1.56. Pursuant to Federal Register Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. Copies of all other cited art references, if any, are attached. No admission is made regarding whether all the submitted references are prior art. Citation of these references is respectfully requested.

Respectfully submitted,

Datod:

Bv:

Jennifer J. Taylo

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